

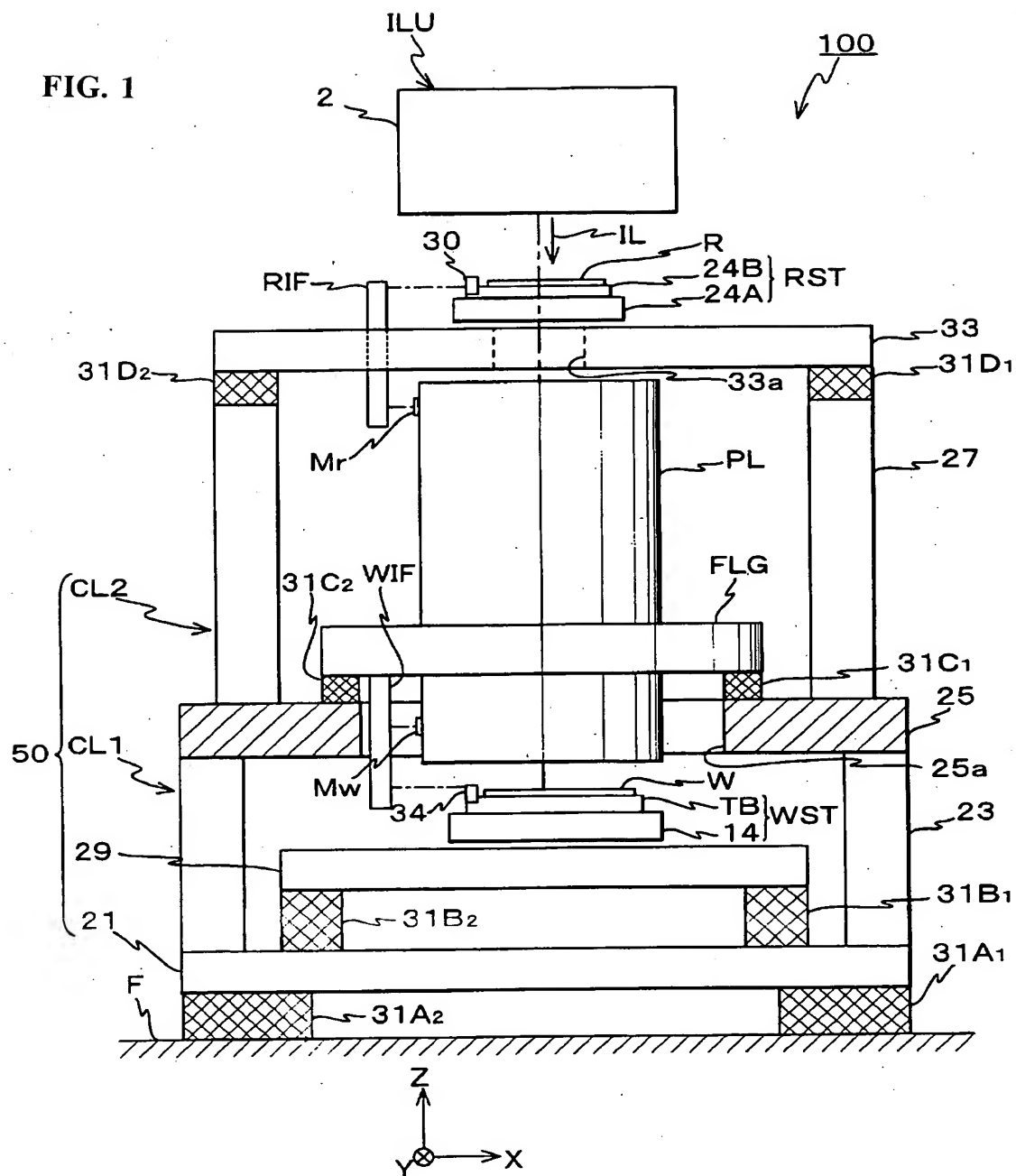
[illegible]

FIG. 2

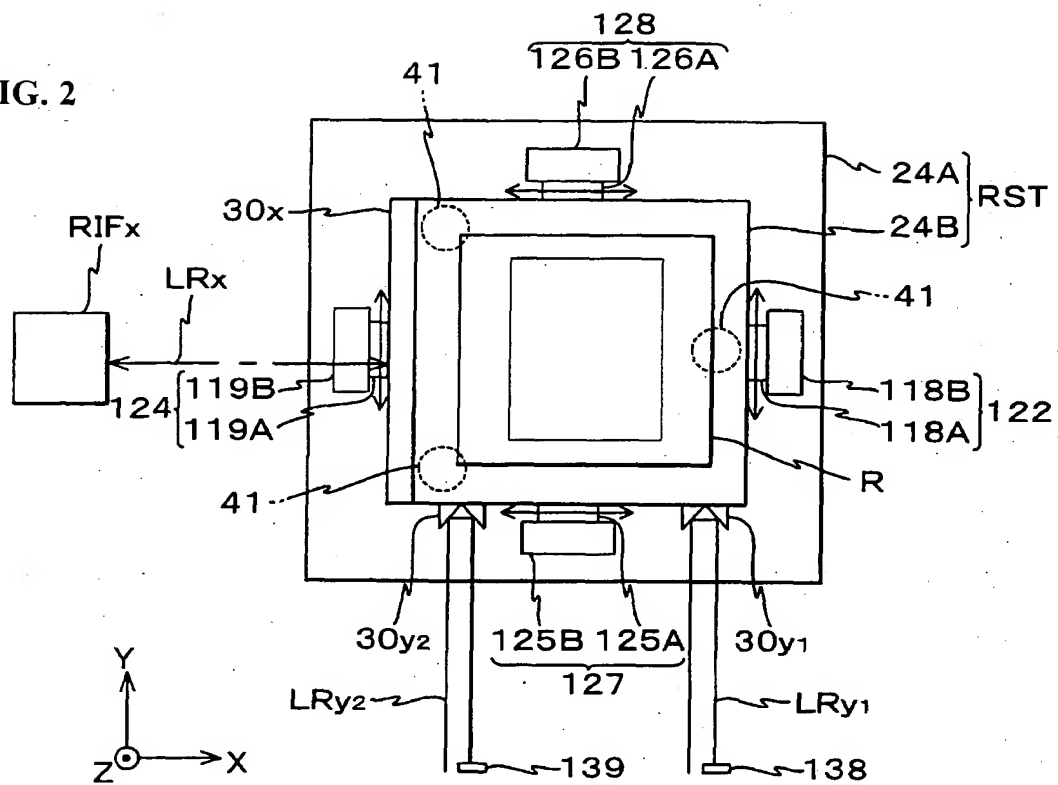


FIG. 3(A)

FIG. 3(A) is a schematic diagram of a square device with a central circular region. The device is defined by a thick border 34X and 34Y. A central circular region contains a central square structure 48Y and four corner square structures 48X1 and 48X2. The corner structures are labeled 49A, 47, 49B, 91A, 91B, 91C, 91D, 92A, 92B, 92C, and 92D. A coordinate system (X, Y, Z) is shown at the top right. A wavy line TB is on the right side. A wavy line WIX is on the left side. A wavy line WIY is at the bottom. A wavy line W is on the left side. A wavy line A is on the top left and top right. A wavy line Z is at the top right.

Fig. 1 is a cross-sectional view of a semiconductor device. It shows a substrate 14 with a top layer TB. A central structure 91A is surrounded by a layer 92A. On the left, a structure 192B is shown with a layer 192A. On the right, a structure 49A is shown with a layer 49B. A layer 47 is also indicated.

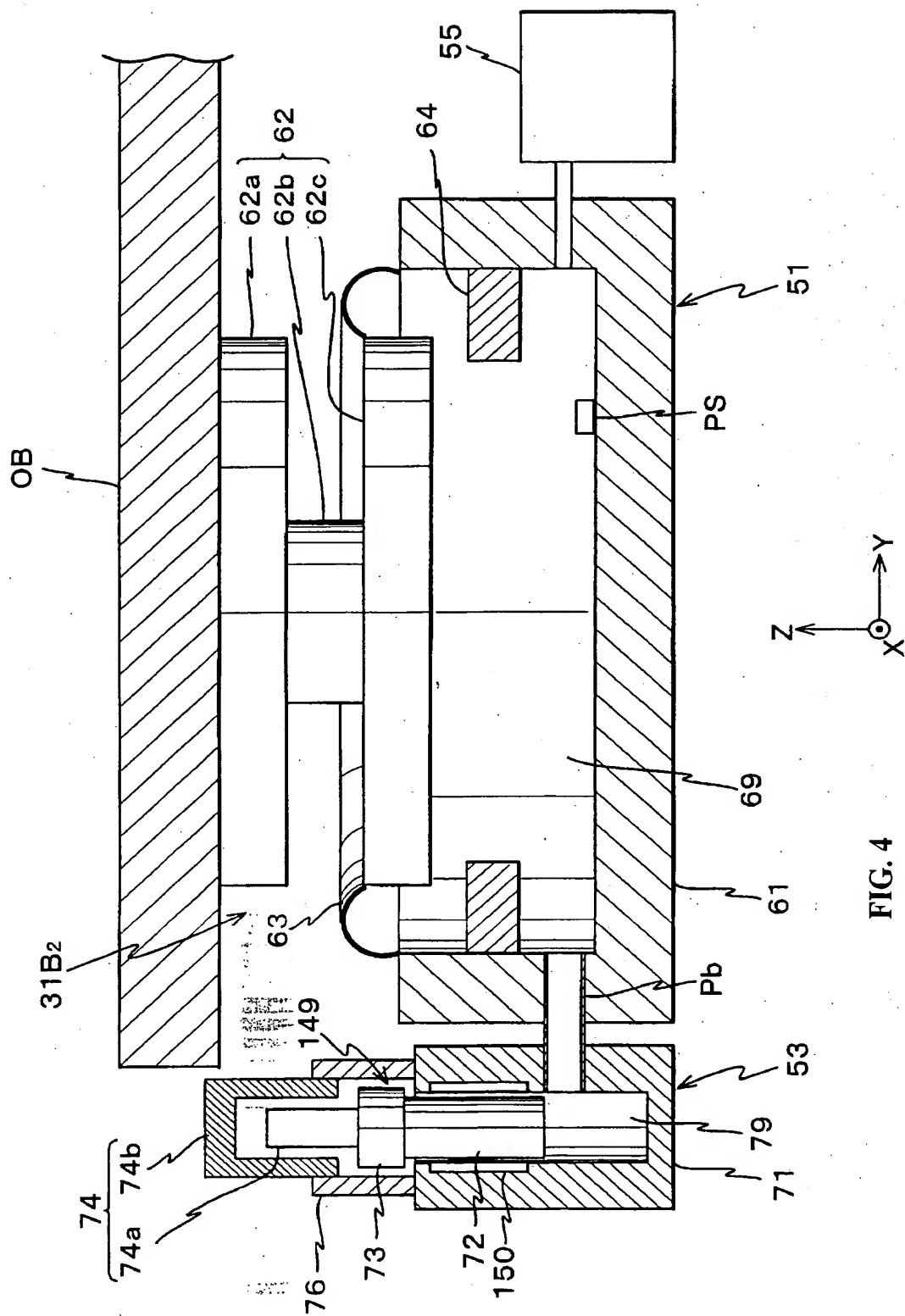


FIG. 4

FIG. 5

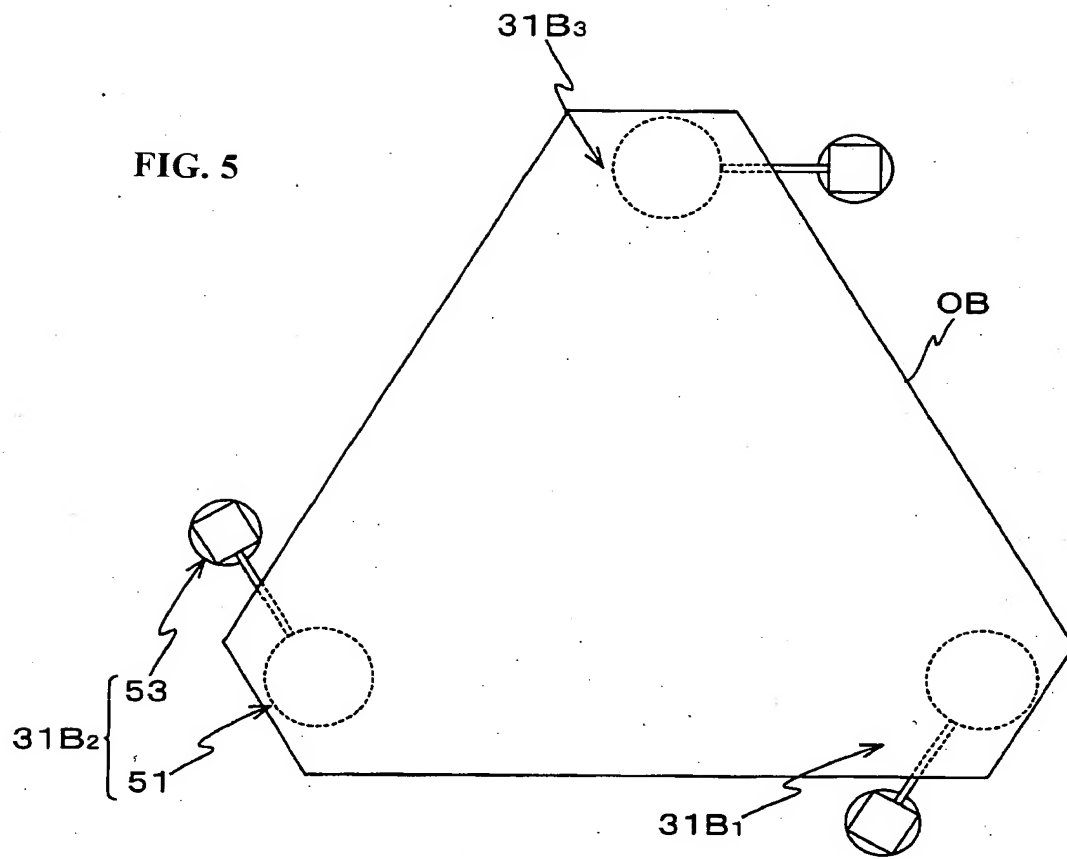


FIG. 6(A)

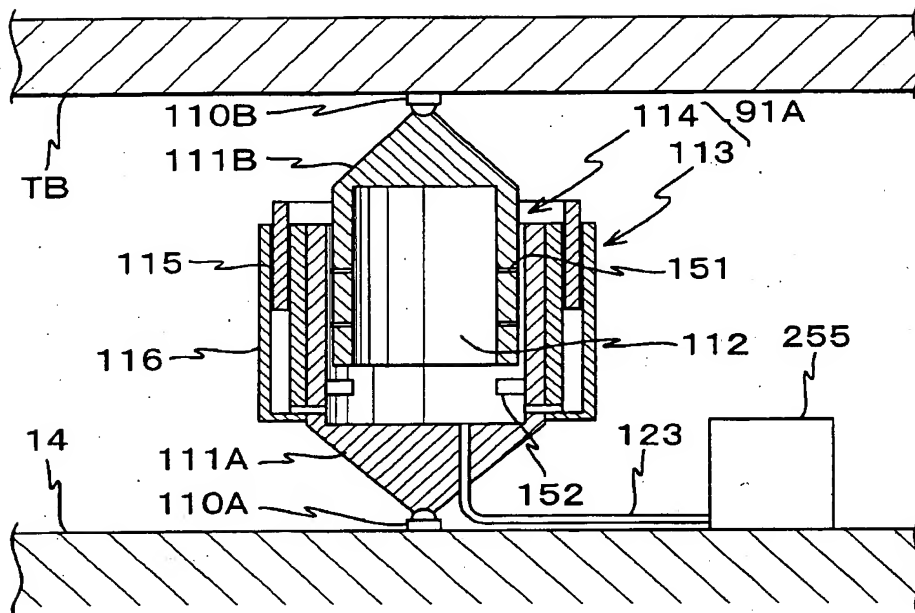


FIG. 6(B)

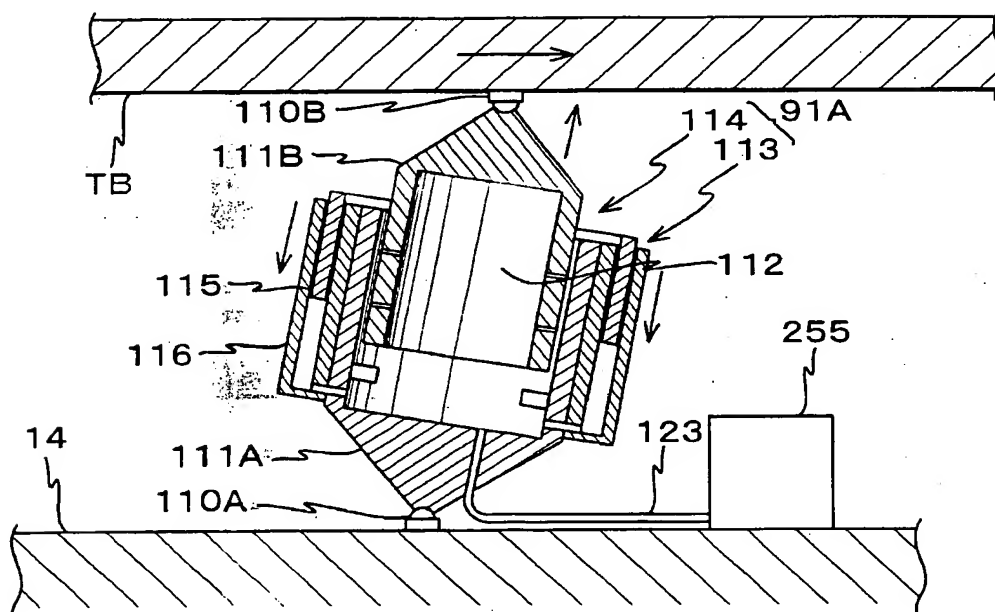


FIG. 7(A)

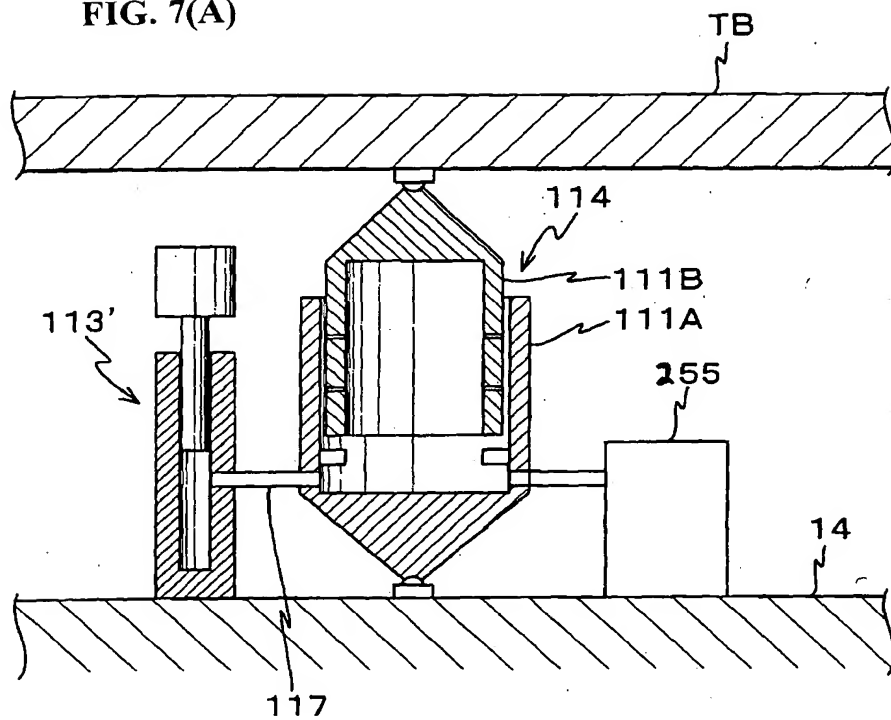


FIG. 7(B)

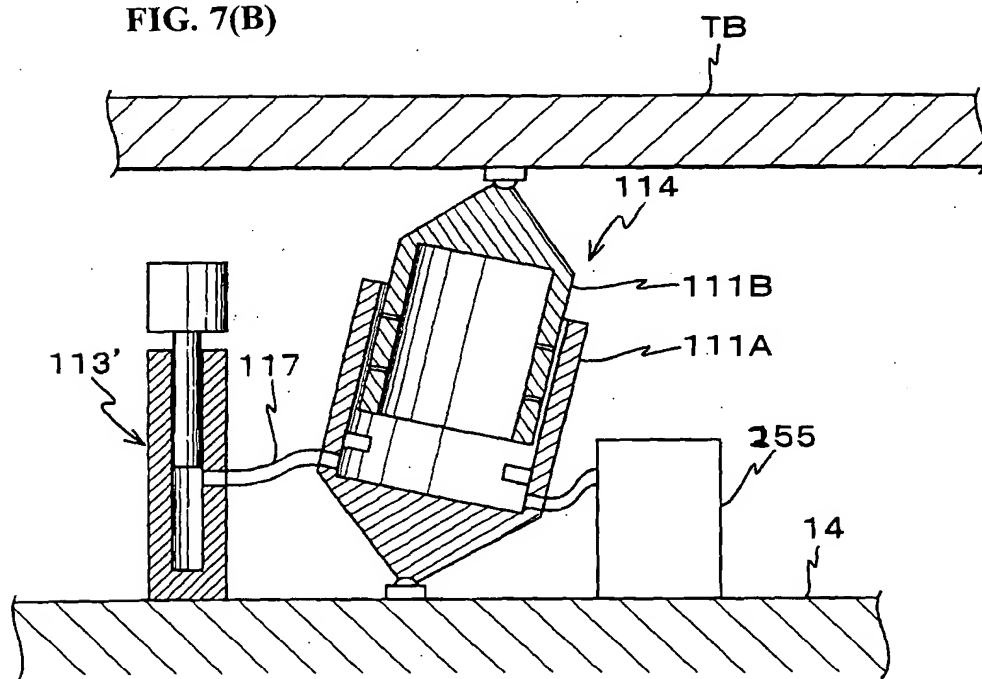


FIG. 8

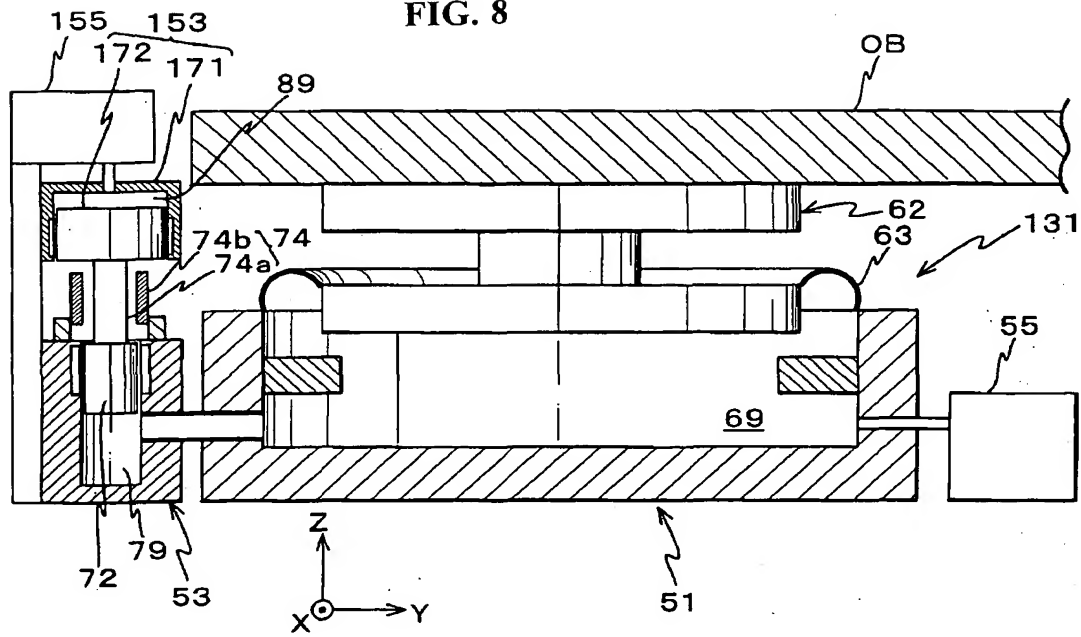


FIG. 9

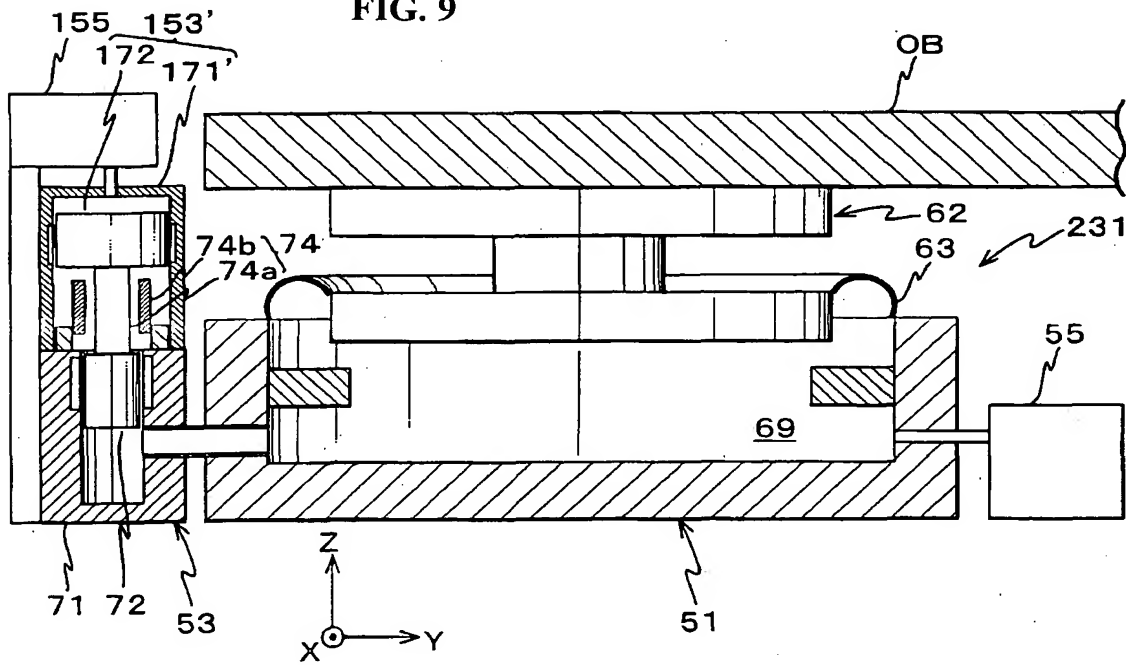


FIG. 10(A)

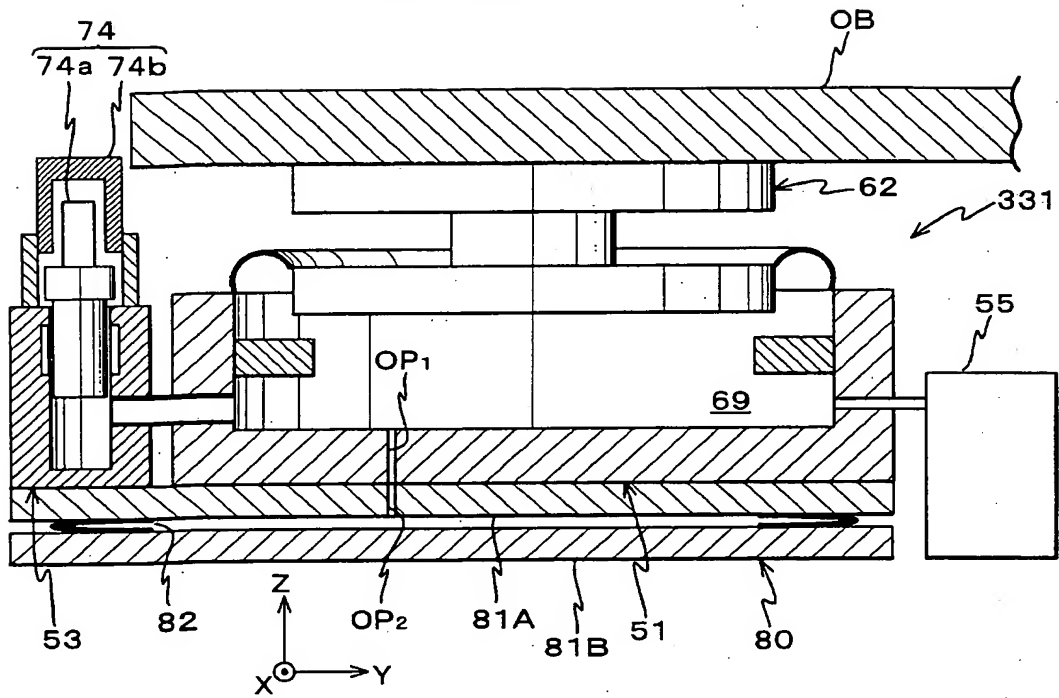


FIG. 10(B)

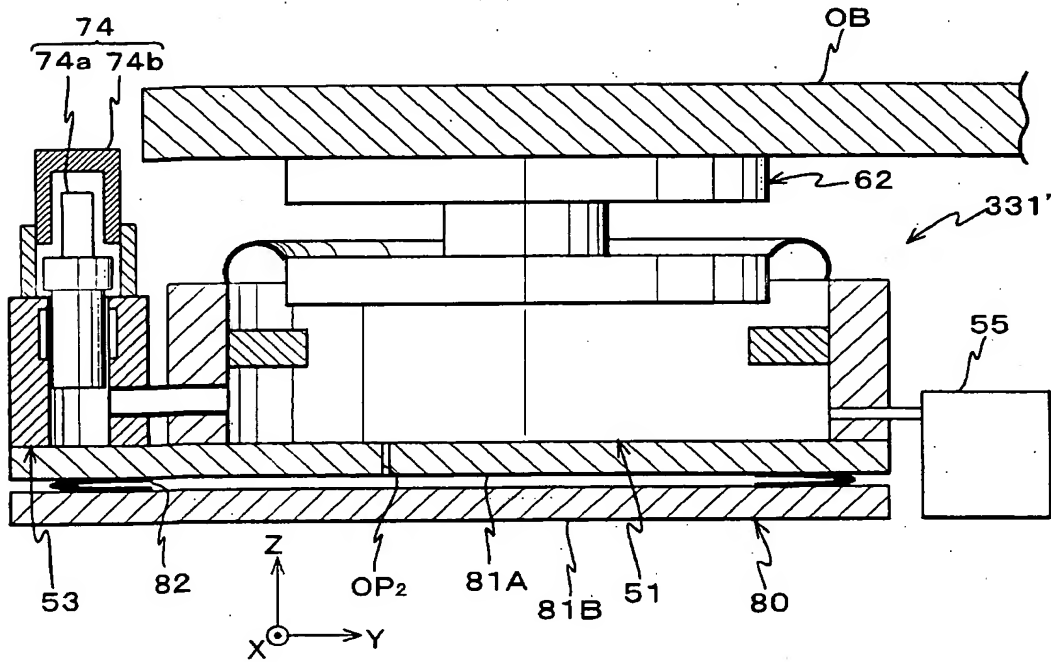


FIG. 11

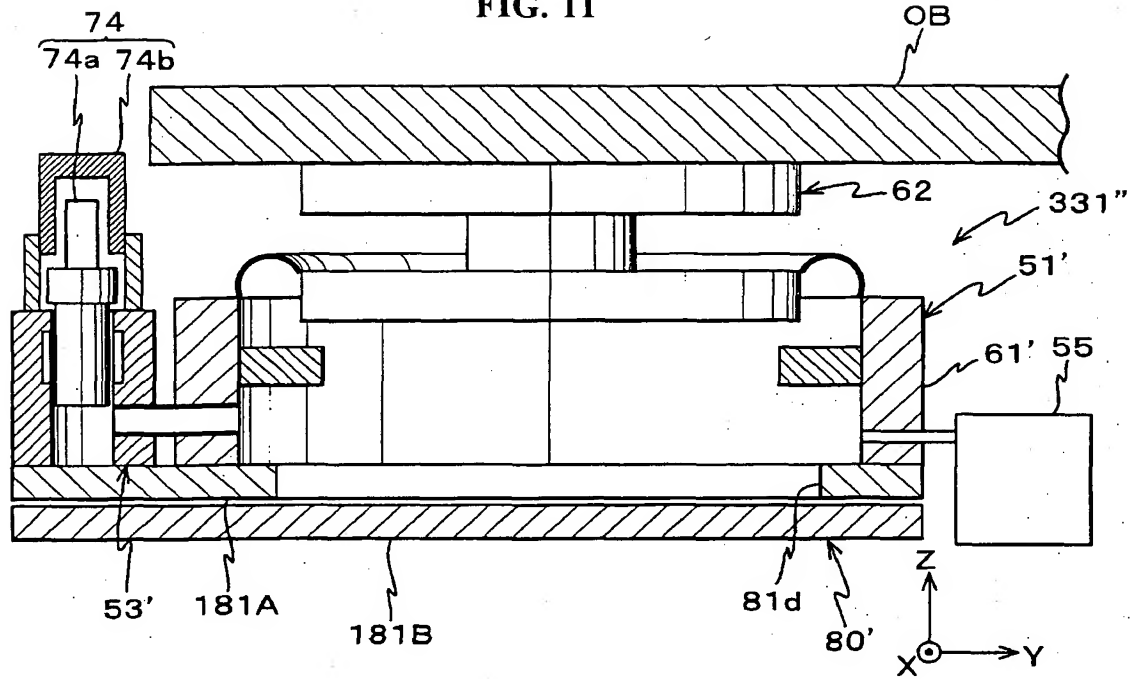


FIG. 12

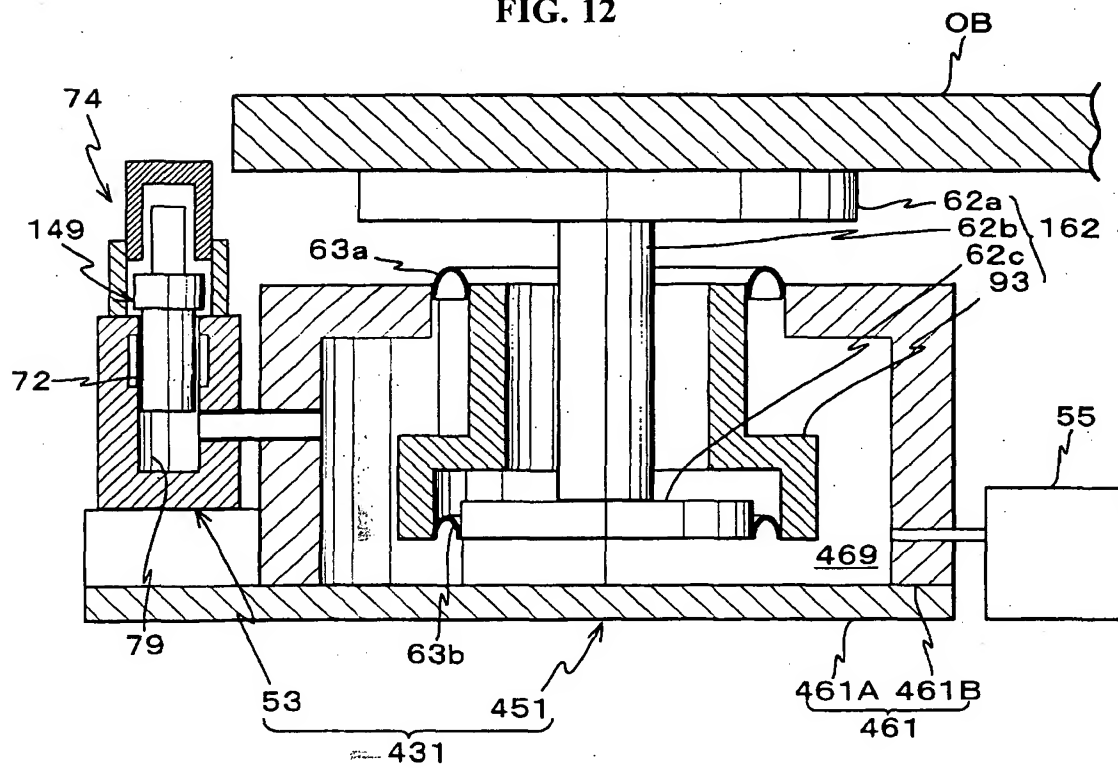


FIG. 13

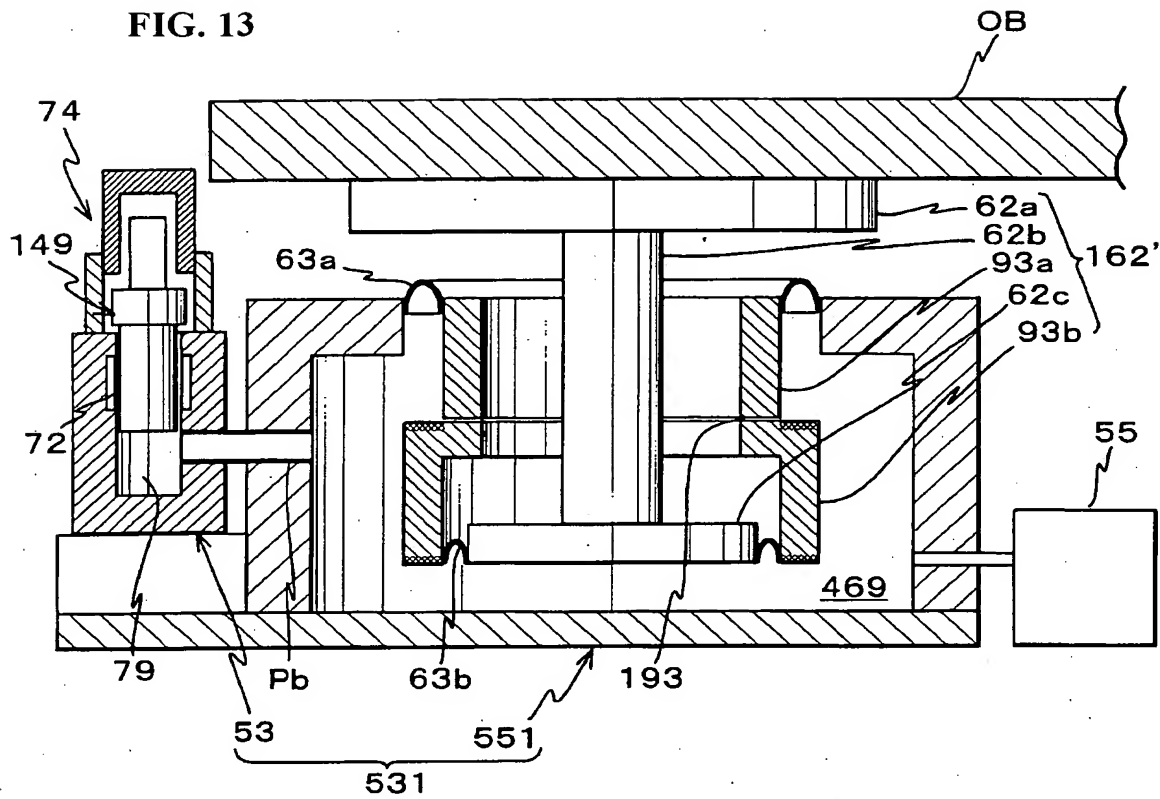


FIG. 14

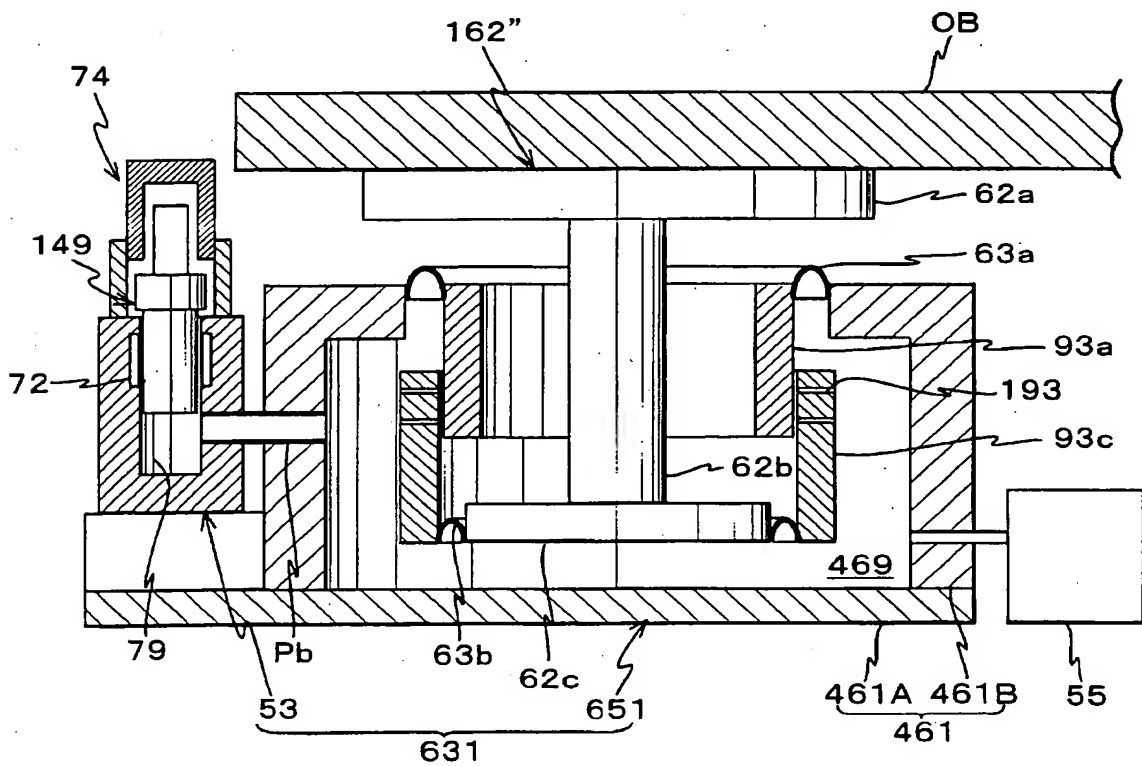


FIG. 15

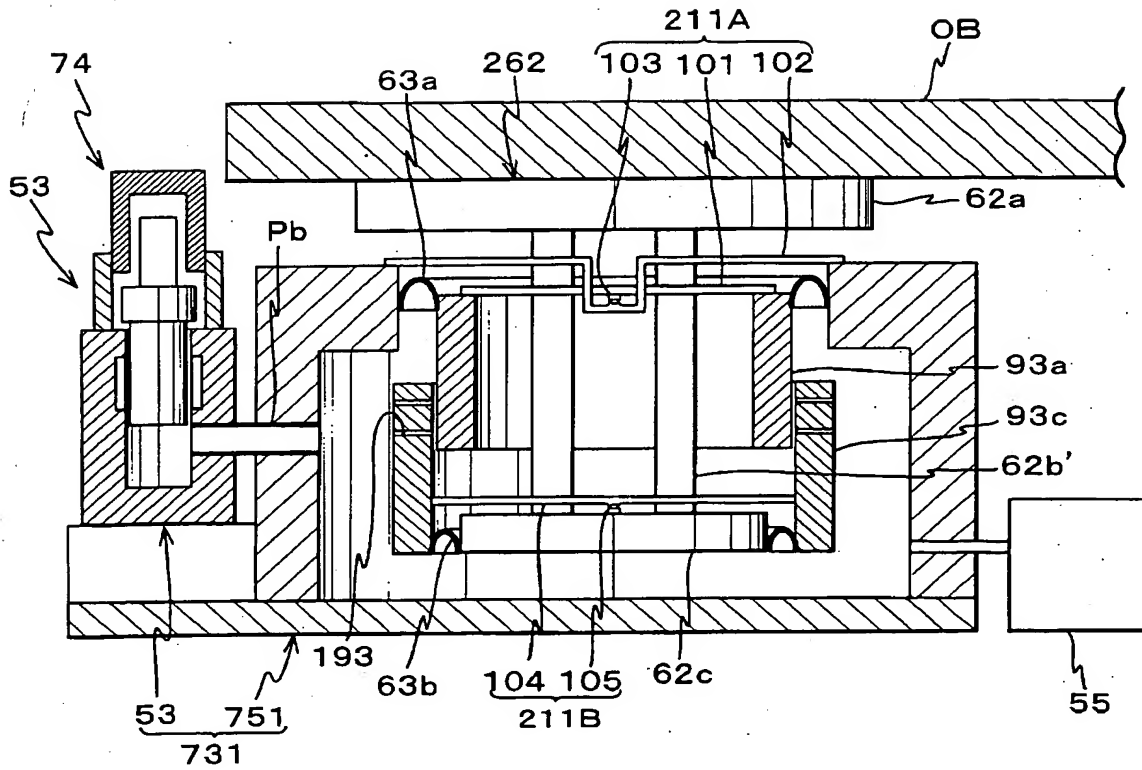


FIG. 16

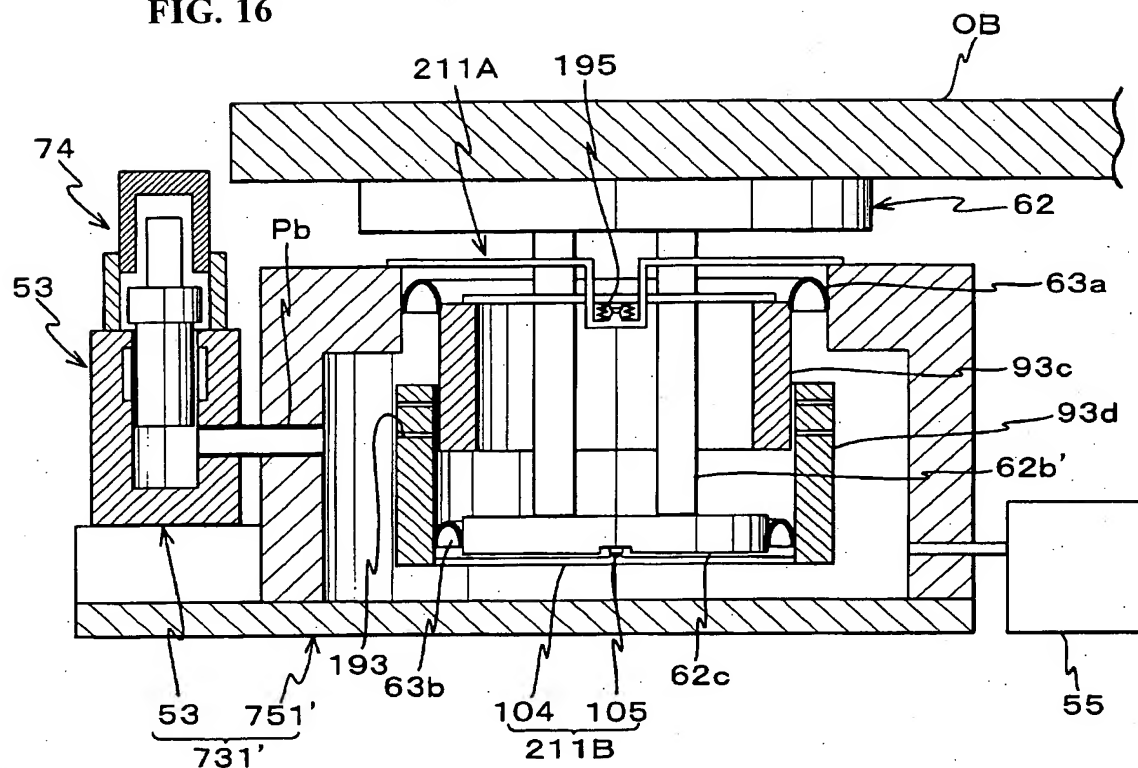


FIG. 17(A)
(PRIOR ART)

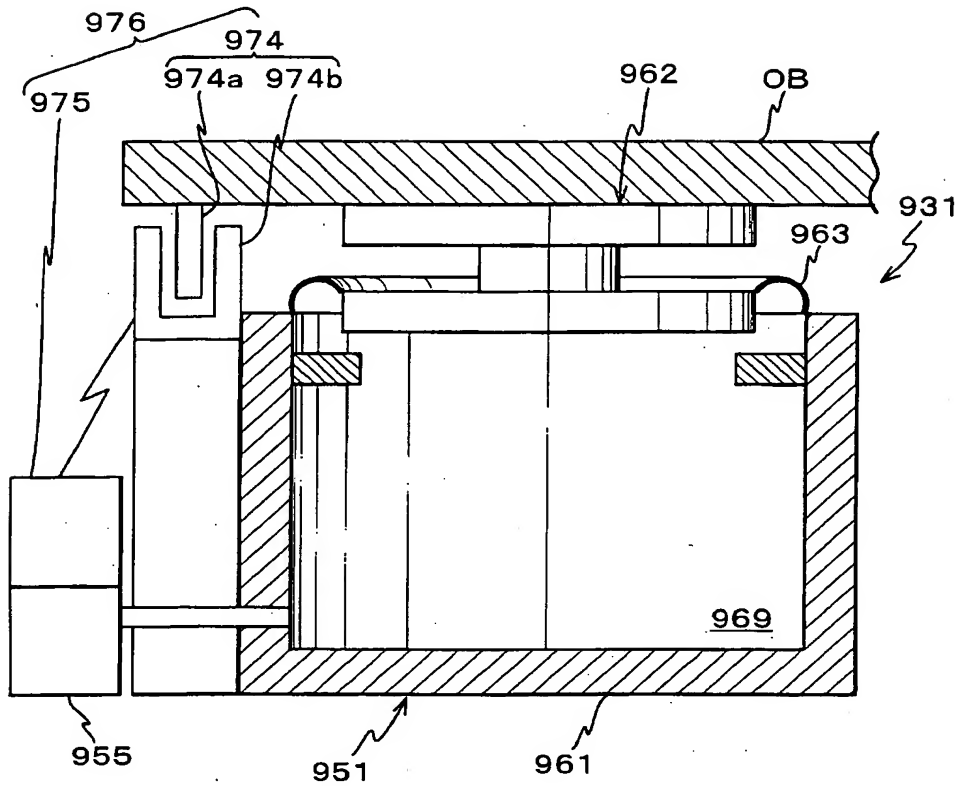


FIG. 17(B)
(PRIOR ART)

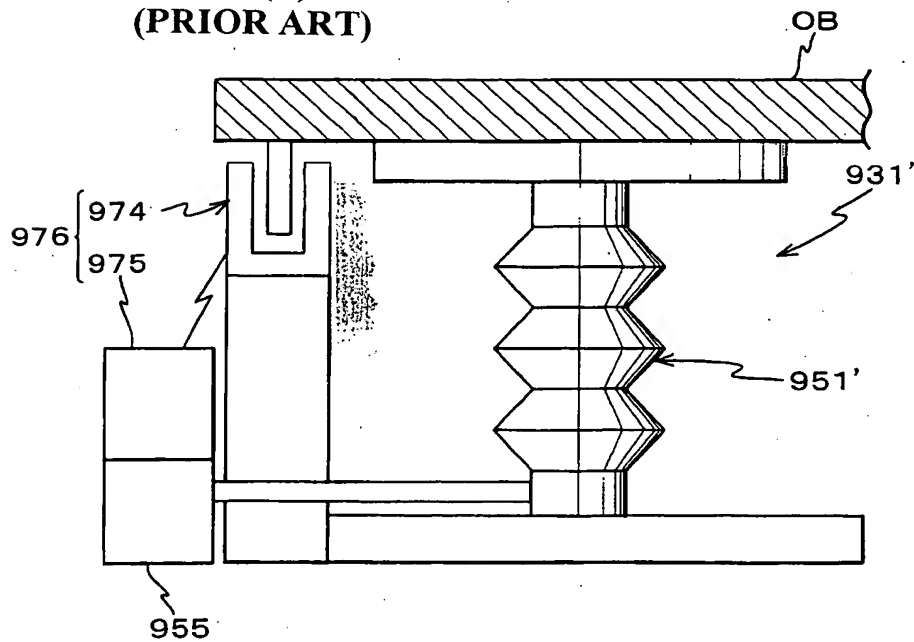


FIG. 18(A)

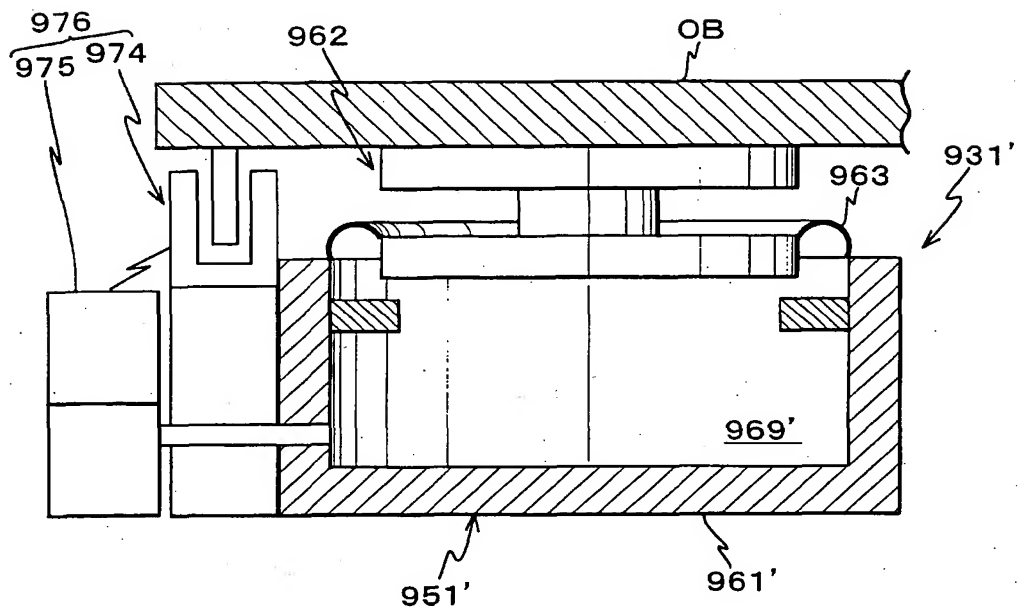


FIG. 18(B)

